

WHAT IS CLAIMED IS:

sub A2 1. A solid-state image pickup apparatus comprising:

5 a solid-state image pickup device in which a first light receiving pixel is disposed in a plurality of lines in one-line units, and a second light receiving pixel capable of being driven independently from said first light receiving pixel is disposed in at least one-line units between first light receiving pixels of the plurality of lines;

10 a drive circuit for driving the first and second light receiving pixels of said solid-state image pickup device and accumulating information charges at mutually different times between first light receiving pixel and second light receiving pixel, as well as transferring and outputting information charges accumulated in said first and second light receiving pixels independently of each other;

15 a timing control circuit for respectively setting the storage time of information charges at the first light receiving pixel and the storage time of information charges at the second light receiving pixel of said solid-state image pickup device; and

20 a signal processing circuit for generating an image signal by adding a first output corresponding to the first light receiving pixel and a second output corresponding to the second light receiving pixel of said solid-state image pickup device.

2. The solid-state image pickup apparatus according to claim 1 wherein said signal processing circuit calculates vertical transfer smear component on the basis of a ratio of said respective accumulation times of information charges at said first and second light receiving pixels of said solid-state image pickup device and of a difference of said first and second outputs corresponding to said first and second light receiving pixels of said solid-state image pickup device.

3. The solid-state image pickup apparatus according to claim 2 wherein said signal processing circuit subtracts said vertical transfer smear component from said image signal on the basis of a ratio of said respective accumulation times of information charges at said first and second light receiving pixels of said solid-state image pickup device and of a difference of said first and second outputs corresponding to said first and second light receiving pixels of said solid-state image pickup device.

4. The solid-state image pickup apparatus comprising:

the solid-state image pickup device having a line of first light receiving pixels and a line of second light receiving pixels disposed so as to respectively correspond to horizontal scanning lines and capable of driving independently of each other said first light receiving pixels and said second light receiving pixels;

the drive circuit for driving said first and second light

receiving pixels so that accumulation of information charges is performed at different times between said first light receiving pixels and said second light receiving pixels, as well as transferring and outputting independently of each other said information charges accumulated in said first and second light receiving pixels and generating the first output according to said information charges accumulated in said first light receiving pixels and the second output according to said information charges accumulated in said second light receiving pixels; and

the signal processing circuit for performing calculation of a smear quantity generated during vertical transfer of said information charges at said solid-state image pickup device on the basis of a ratio of said respective accumulation times of said information charges at said first and second light receiving pixels and of said first output and said second output.

5. The solid-state image pickup apparatus according to claim 4 wherein said signal processing circuit performs calculation of said smear quantity for every pair of said first light receiving pixel and said second light receiving pixel disposed so as to be in mutual proximity on same vertical transfer path.

6. The solid-state image pickup apparatus according to claim 4 wherein said signal processing circuit removes smear

component included in at least said first output or said second output on the basis of said smear quantity of said calculation.

5 7. The solid-state image pickup apparatus comprising:

the solid-state image pickup device having a line of first light receiving pixels and a line of second light receiving pixels disposed so as to respectively correspond to horizontal scanning lines and capable of driving independently of each other said first light receiving pixels and said second light receiving pixels;

10 the drive circuit for driving said first and second light receiving pixels so that accumulation of information charges is performed at different times between said first light receiving pixels and said second light receiving pixels, as well as transferring and outputting independently of each other said information charges accumulated in said first and second light receiving pixels and generating the first output according to said information charges accumulated in said first light receiving pixels and the second output according to said information charges accumulated in said second light receiving pixels; and

20 the signal processing circuit for generating an image signal by adding together said first output and said second output corresponding to identical horizontal scanning lines.

25 8. The solid-state image pickup apparatus according to claim

7 wherein said signal processing circuit performs calculation
of smear quantity generated during vertical transfer of said
information charges at said solid-state image pickup device on
the basis of a ratio of said respective accumulation times of
5 said information charges at said first and second light
receiving pixels and of said first output and said second
output.

9. The solid-state image pickup apparatus according to claim
10 8 wherein said signal processing circuit performs calculation
of said smear quantity for every pair of said first light
receiving pixel and said second light receiving pixel disposed
so as to be in mutual proximity on same vertical transfer
path.

15 10. The solid-state image pickup apparatus according to claim
8 wherein said signal processing circuit removes smear
component included in said image signal on the basis of said
smear quantity of said calculation.